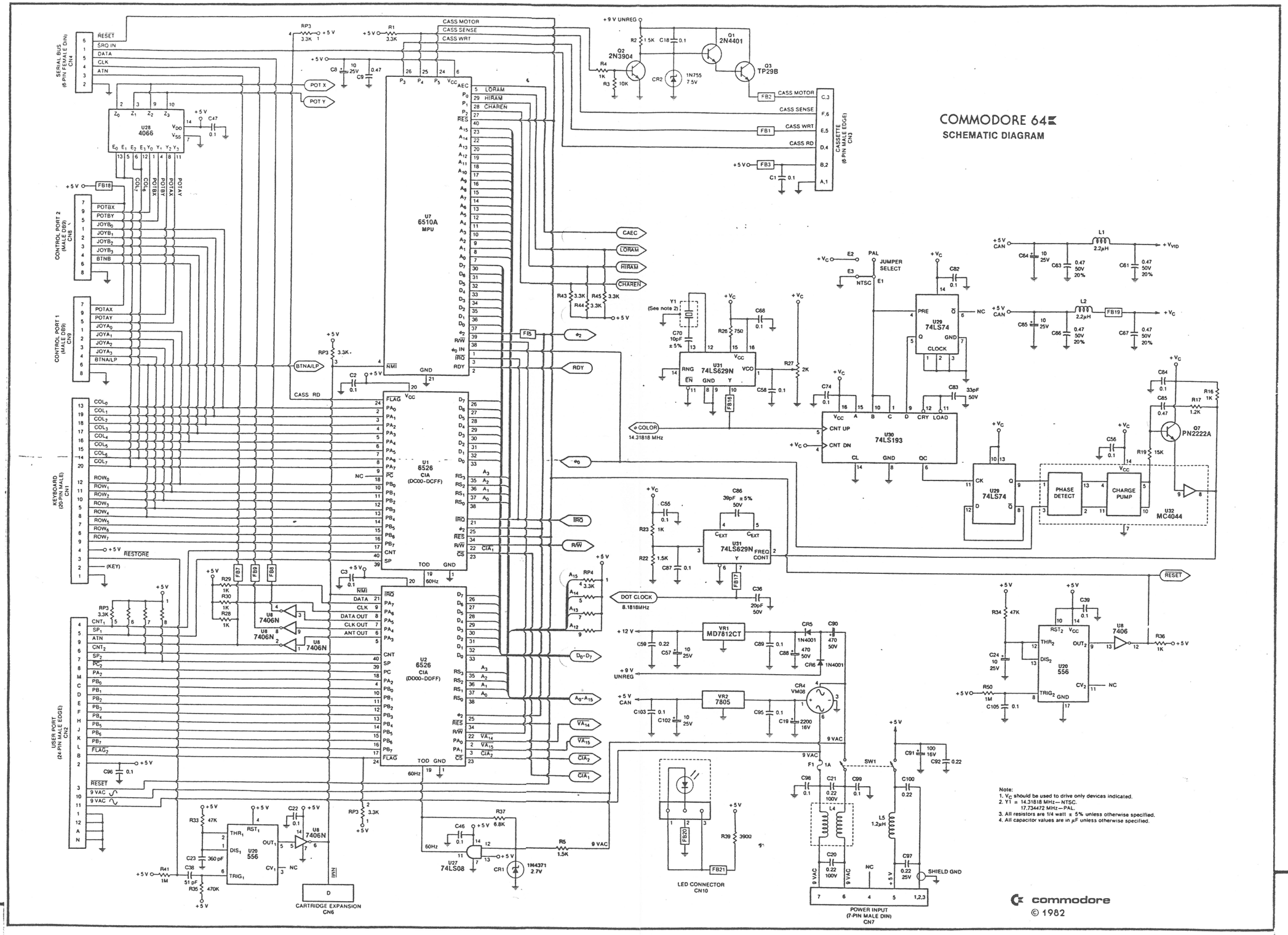
COMMODORE 64
SCHEMATIC DIAGRAM



Note:
1. V_C should be used to drive only devices indicated.
2. Y1 = 14.31818 MHz—NTSC.
   17.734472 MHz—PAL.
3. All resistors are 1/4 watt ± 5% unless otherwise specified.
4. All capacitor values are in μF unless otherwise specified.